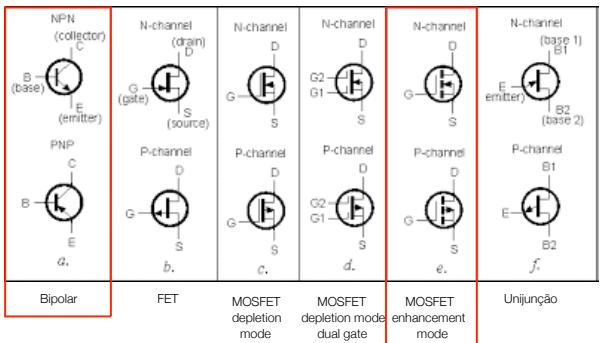


Interface com o mundo real

Jun Okamoto Jr.

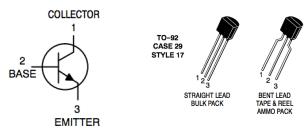
Transistores



Bipolar – NPN

**BC337, BC337-25,
BC337-40**

Amplifier Transistors



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V_{CEO}	45	Vdc
Collector – Base Voltage	V_{CBO}	50	Vdc
Emitter – Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	I_C	800	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to +150	°C

Bipolar — NPN

ON CHARACTERISTICS		Min.	Typ.	Max.	Units
DC Current Gain ($I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}$)	BC327 BC327-25 BC327-40	β_{FE}	100 180 250 60	- - - -	630 400 630 -
Base-Emitter On Voltage ($I_C = 300 \text{ mA}, V_{CE} = 1.0 \text{ V}$)		$V_{BE(on)}$	-	-	1.2 V_{dc}
Collector-Emitter Saturation Voltage ($I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$)		$V_{CE(\text{sat})}$	-	-	0.7 V_{dc}
SMALL-SIGNAL CHARACTERISTICS					
Output Capacitance ($V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$)		C_{ob}	-	15	- pF
Current-Gain Bandwidth Product ($I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}, f = 100 \text{ MHz}$)		f_T	-	210	- MHz

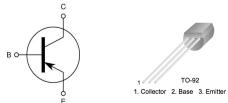
Bipolar — PNP



BC327/328

Switching and Amplifier Applications

- Suitable for AF-Drive stages and low power output stages
- Complement to BC327/BC328



Absolute Maximum Ratings $T_a=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage : BC327 : BC328 $I_C = -0.1 \text{ mA}, V_{BE}=0$	-50 -30	V
V_{CEO}	Collector-Emitter Voltage : BC327 : BC328 $I_C = -10 \text{ mA}, I_B=0$	-45 -25	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current (DC)	-600	mA
P_c	Collector Power Dissipation	625	mW
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	-55 ~ 150	$^\circ\text{C}$

MOSFET — enhancement mode — N-channel

2N7002P

60 V, 360 mA N-channel Trench MOSFET

Rev. 02 — 29 July 2010



Table 1. Quick reference data

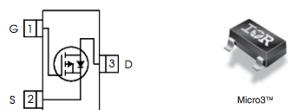
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_{amb} = 25^\circ\text{C}$	-	-	60	V
V_{GS}	gate-source voltage		-20	-	20	V
I_D	drain current	$V_{GS} = 10 \text{ V}; T_{amb} = 25^\circ\text{C}$	[1]	-	-	360 mA
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 500 \text{ mA}; T_j = 25^\circ\text{C}; \text{ pulsed; } t_g \leq 300 \mu\text{s; } \delta \leq 0.01$	-	1	1.6	Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

MOSFET – enhancement mode – P-channel

IRLML6401PbF

HEXFET® Power MOSFET



Absolute Maximum Ratings

Parameter	Max.	Units
V_{DS}	-12	V
$I_D @ T_A = 25^\circ C$	-4.3	
$I_D @ T_A = 70^\circ C$	-3.4	A
I_{DM}	-34	
$P_D @ T_A = 25^\circ C$	1.3	
$P_D @ T_A = 70^\circ C$	0.8	W
Linear Derating Factor	0.01	W/ $^\circ C$
E_{AS}	33	mJ
V_{GS}	± 8.0	V
T_J, T_{STG}	-55 to +150	$^\circ C$

MOSFET – enhancement mode – P-channel

Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise specified)

Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{BR(DSS)}$	Drain-to-Source Breakdown Voltage	-12	—	V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{BR(DSS)} / \Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.007	V/ $^\circ C$	Reference to $25^\circ C, I_D = -1mA$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	0.050	Ω	$V_{GS} = -4.5V, I_D = -4.3A$
$V_{GS(th)}$	Gate Threshold Voltage	-0.40	-0.55	V	$V_{GS} = V_{DS}, I_D = -250\mu A$
g_f	Forward Transconductance	8.6	—	S	$V_{GS} = -10V, I_D = -4.3A$
I_{BS}	Drain-to-Source Leakage Current	—	-1.0	μA	$V_{GS} = -12V, V_{DS} = 0V$
I_{BSS}	Drain-to-Source Leakage Current	—	-25	μA	$V_{GS} = -9.6V, V_{DS} = 0V, T_J = 55^\circ C$
I_{GSS}	Gate-to-Source Forward Leakage	—	-100	nA	$V_{GS} = -8.0V$
I_{GRS}	Gate-to-Source Reverse Leakage	—	100	nA	$V_{GS} = 8.0V$

MOSFET – enhancement mode – N-channel (2A)

MCH3481

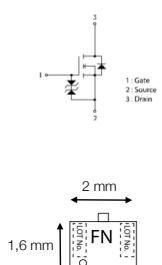
**Power MOSFET
20V, 104m Ω , 2A, Single N-Channel**

SPECIFICATIONS ABSOLUTE MAXIMUM RATING at $T_A = 25^\circ C$ (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V_{DSS}	20	V
Gate to Source Voltage	V_{GSS}	± 9	V
Drain Current (DC)	I_D	2	A
Drain Current (Pulse) $PW \leq 10\mu s$, duty cycle $\leq 1\%$	I_{DP}	8	A
Power Dissipation When mounted on ceramic substrate (900mm 2 x 0.8mm)	P_D	0.8	W
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature	T_{STG}	-55 to +150	$^\circ C$

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed; damage may occur and reliability may be affected.

ELECTRICAL CONNECTION
N-Channel



MOSFET – enhancement mode – N-channel (2A)

Parameter	Symbol	Conditions	Value		
			min	typ	
			max	Unit	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}, V_{GS}=0\text{V}$	20	V	
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$	1	μA	
Gate to Source Leakage Current	I_{GSS}	$V_{GS}=\pm 2\text{V}, V_{DS}=0\text{V}$	± 10	μA	
Gate Threshold Voltage	$V_{G(th)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	0.3	V	
Forward Transconductance	g_{FS}	$V_{DS}=10\text{V}, I_D=1\text{mA}$	2.4	S	
	$R_{DS(on)1}$	$I_D=1\text{A}, V_{GS}=4.5\text{V}$	80	$\text{m}\Omega$	
Static Drain to Source On-State Resistance	$R_{DS(on)2}$	$I_D=0.5\text{A}, V_{GS}=2.5\text{V}$	105	$\text{m}\Omega$	
	$R_{DS(on)3}$	$I_D=0.3\text{A}, V_{GS}=1.8\text{V}$	135	$\text{m}\Omega$	
	$R_{DS(on)4}$	$I_D=0.1\text{A}, V_{GS}=1.2\text{V}$	270	$\text{m}\Omega$	
Input Capacitance	C_{iss}		175	pF	
Output Capacitance	C_{oss}	$V_{DS}=10\text{V}, f=1\text{MHz}$	30	pF	
Reverse Transfer Capacitance	C_{rss}		25	pF	
Turn-ON Delay Time	$t_{f(on)}$		6.6	ns	
Rise Time	t_r	See specified Test Circuit	27	ns	
Turn-OFF Delay Time	$t_{f(off)}$		28	ns	
Fall Time	t_f		19	ns	
Total Gate Charge	Q_g		2.9	nC	
Gate to Source Charge	Q_{gs}	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=2\text{A}$	0.46	nC	
Gate to Drain "Miller" Charge	Q_{gd}		0.53	nC	
Forward Diode Voltage	V_{SD}	$I_S=2\text{A}, V_{GS}=0\text{V}$	0.85	1.2	V

Note 2 - Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted.
Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

MOSFET – enhancement mode – N-channel (2A)

- Sistema para caracterização dos motores de PMR3404
- Acionamento PWM do motor do PI-7

