

Padrões de Documentação

1 Objetivos deste tópico

Ao final do estudo deste tópico você saberá:

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Leitura recomendada : seções do livro do Wakerly

- 6.1 - Documentation Standards
 - 6.1.1 - Block Diagrams
 - 6.1.2 - Gate Symbols
 - 6.1.3 - Signal Names and Active Levels
 - 6.1.4 - Active Levels for Pins
 - 6.1.5 - Bubble-to-Bubble Logic Design
 - 6.1.7 - Drawing Layout
 - 6.1.8 - Buses
 - 6.1.9 - Additional Schematic Information
- 6.2 - Circuit Timing
 - 6.2.1 - Timing Diagrams
 - 6.2.2 - Propagation Delay
 - 6.2.3 - Timing Specifications
 - 6.2.4 - Timing Analysis

Keywords: circuit specification, block diagram, schematic diagram, logic diagram, bill of materials, timing diagram, bus, inversion bubble, active level, active high, active low, assert, negate, deassert, _L suffix

2 Exercícios

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