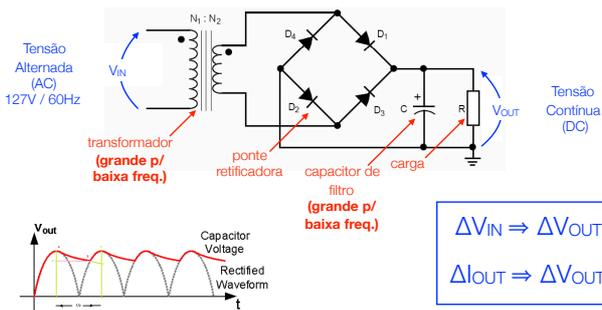


Baterias e Conversores DC-DC

Jun Okamoto Jr.

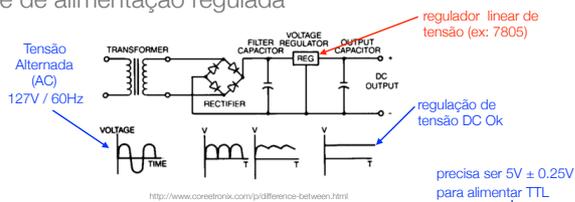
Revisão: Conversor AC-DC

- Fonte de alimentação não regulada



Revisão: Conversor AC-DC

- Fonte de alimentação regulada



recommended operating conditions (see Note 3)

	SN7400			SN7400			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH} High-level input voltage	2						V
V _{IL} Low-level input voltage		0.8			0.8		V
I _{OH} High-level output current		-16			-16		mA
I _{OL} Low-level output current		16			16		mA
T _A Operating free-air temperature	-55	125	0		70		°C

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number: SBOA004.

Regulador linear de tensão (7805)

electrical characteristics at specified virtual junction temperature, $V_I = 10\text{ V}$, $I_O = 500\text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T_J	μA7805C			UNIT
			MIN	TYP	MAX	
Output voltage	$I_O = 5\text{ mA to }1\text{ A}$, $V_I = 7\text{ V to }20\text{ V}$, $P_D < 10\text{ W}$	25°C 0°C to 125°C	4.8 4.75	5 5.25	5.2	V
Input voltage regulation	$V_I = 7\text{ V to }25\text{ V}$	25°C	3	100		mV
Ripple rejection	$V_I = 8\text{ V to }18\text{ V}$, $f = 120\text{ Hz}$	0°C to 125°C	62	78		dB
Output voltage regulation	$I_O = 5\text{ mA to }1.5\text{ A}$	25°C	15	100		mV
Output resistance	$f = 1\text{ kHz}$	0°C to 125°C	0.017			Ω
Temperature coefficient of output voltage	$I_O = 5\text{ mA}$	0°C to 125°C	-1.1			mV/°C
Output noise voltage	$f = 10\text{ Hz to }100\text{ kHz}$	25°C	40			μV
Dropout voltage	$I_O = 1\text{ A}$	25°C	2			V
Bias current		25°C	4.2	8		mA
Bias current change	$V_I = 7\text{ V to }25\text{ V}$	0°C to 125°C		1.3		mA
Short-circuit output current	$I_O = 5\text{ mA to }1\text{ A}$	25°C	700			mA
Peak output current		25°C	2.2			A

[†] Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

recommended operating conditions

PARAMETER	TEST CONDITIONS	μA7805C			UNIT
		MIN	MAX	UNIT	
V_I Input voltage		μA7805C	7	25	V
		μA7808C	10.5	25	
		μA7810C	12.5	28	
		μA7812C	14.5	30	
		μA7815C	17.5	30	
I_O Output current		μA7805C	27	38	A
T_J Operating virtual junction temperature		μA7800C series	0	125	°C

μA7800 data sheet

Regulador linear de tensão (7805)

electrical characteristics at specified virtual junction temperature, $V_I = 10\text{ V}$, $I_O = 500\text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T_J	μA7805C			UNIT
			MIN	TYP	MAX	
Output voltage	$I_O = 5\text{ mA to }1\text{ A}$, $V_I = 7\text{ V to }20\text{ V}$, $P_D < 10\text{ W}$	25°C 0°C to 125°C	4.8 4.75	5 5.25	5.2	V
Input voltage regulation	$V_I = 7\text{ V to }25\text{ V}$	25°C	3	100		mV
Ripple rejection	$V_I = 8\text{ V to }18\text{ V}$, $f = 120\text{ Hz}$	0°C to 125°C	62	78		dB
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Dropout voltage	$I_O = 1\text{ A}$	25°C	2			V
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Bias current change	$V_I = 7\text{ V to }25\text{ V}$	0°C to 125°C		1.3		mA
Short-circuit output current	$I_O = 5\text{ mA to }1\text{ A}$	25°C	700			mA
Peak output current		25°C	2.2			A

[†] Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

recommended operating conditions

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		MIN	MAX	UNIT	
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		μA7810C	12.5	28	
		μA7812C	14.5	30	
		μA7815C	17.5	30	
I_O Output current		μA7805C	27	38	A
T_J Operating virtual junction temperature		μA7800C series	0	125	°C

μA7800 data sheet

Ok para TTL

Regulador linear de tensão (7805)

electrical characteristics at specified virtual junction temperature, $V_I = 10\text{ V}$, $I_O = 500\text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T_J	μA7805C			UNIT
			MIN	TYP	MAX	
Output voltage	$I_O = 5\text{ mA to }1\text{ A}$, $V_I = 7\text{ V to }20\text{ V}$, $P_D < 10\text{ W}$	25°C 0°C to 125°C	4.8 4.75	5 5.25	5.2	V
Input voltage regulation	$V_I = 7\text{ V to }25\text{ V}$	25°C	3	100		mV
Ripple rejection	$V_I = 8\text{ V to }18\text{ V}$, $f = 120\text{ Hz}$	0°C to 125°C	62	78		dB
Output voltage regulation	$I_O = 5\text{ mA to }1.5\text{ A}$	25°C	15	100		mV
Output resistance	$f = 1\text{ kHz}$	0°C to 125°C	0.017			Ω
Temperature coefficient of output voltage	$I_O = 5\text{ mA}$	0°C to 125°C	-1.1			mV/°C
Output noise voltage	$f = 10\text{ Hz to }100\text{ kHz}$	25°C	40			μV
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Short-circuit output current	$I_O = 5\text{ mA to }1\text{ A}$	25°C	700			mA
Peak output current		25°C	2.2			A

[†] Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

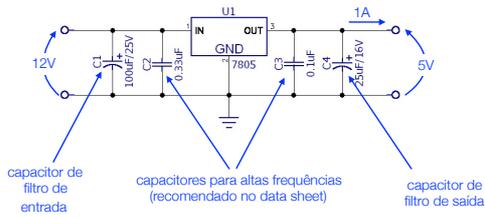
recommended operating conditions

PARAMETER	TEST CONDITIONS	μA7805C			UNIT
		MIN	MAX	UNIT	
V_I Input voltage		μA7805C	7	25	V
		μA7808C	10.5	25	
		μA7810C	12.5	28	
		μA7812C	14.5	30	
		μA7815C	17.5	30	
I_O Output current		μA7805C	27	38	A
T_J Operating virtual junction temperature		μA7800C series	0	125	°C

μA7800 data sheet

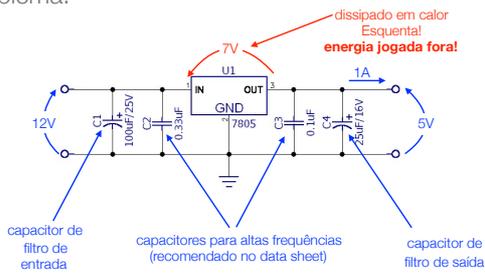
Atenção para as tensões mínima e máxima de entrada (12V → Ok)

Regulador linear de tensão (7805)



Regulador linear de tensão (7805)

- O problema:



Regulador linear de tensão (7805)

- As soluções:

- Baixar a tensão de entrada p/ o mínimo: 7V
 - Mesmo assim fica queda de tensão de 2V sobre o 7805

- Baixar a tensão de entrada para 5.5V no mínimo e usar um Low Dropout (LDO)

- Usar outra coisa:

- Fonte chaveada

ST **L4941**
Very low drop 1A regulator

Feature summary

- Low dropout voltage (450mV typ. at 1A)
- Very low quiescent current
- Thermal shutdown
- Short circuit protection
- Reverse polarity protection

Description

The L4941 is a three terminal 5V positive regulators available in TO-220 and DPAK packages, making it useful in a wide range of industrial and consumer applications. Thanks to its very low input/output voltage drop, these devices are particularly suitable for battery powered equipments, reducing consumption and prolonging battery life. It employs internal current limiting, auto-lockout circuit, thermal shut-down and safe area protection.

TO-220 DPAK

L4941 data sheet

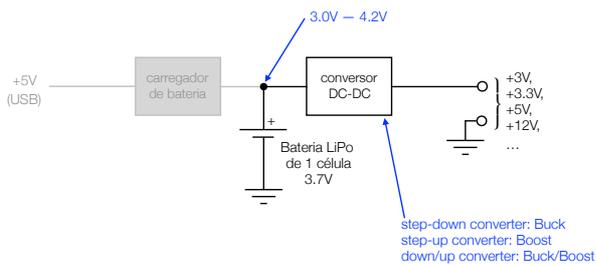
Fonte chaveada

- Trabalha em alta frequência
 - Reduz o tamanho do transformador e do capacitor
- Eficiência de 80-95% (típico)
- Ótima regulação de tensão e corrente
- Projeto mais complexo
- Maior custo do que fontes lineares



Usando conversores DC-DC

- Sistema de alimentação por bateria



Baterias

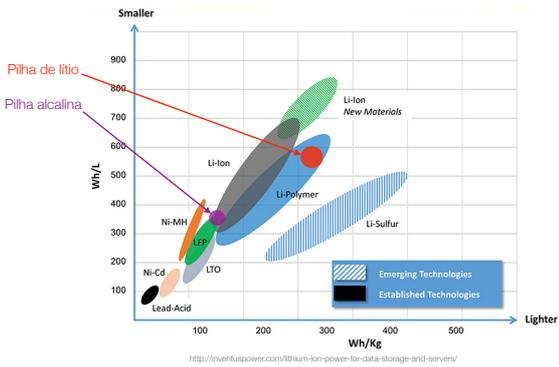
- Algumas características

Química	Voltagem da célula	Auto-descarga	Memória	Número de ciclos	Temperatura	Peso
(1) Alcalina	1.5V	<0.3% / mês	n/a	n/a	-15°C - 55°C	médio
(2) Li/MnO ₂	3.0V	0.5% / ano	n/a	n/a	-30°C - 60°C	muito leve
NiCd	1.2V	20% / mês	sim	< 800	-20°C - 60°C	pesado
NiMH	1.2V	30% / mês	um pouco	< 500	-20°C - 70°C	médio
Chumbo - ácido	2.1V	3-20% / mês	não	< 350	-35°C - 45°C	pesado
Li-ion	3.6V	5-10% / mês	não	500-1000	-40°C - 70°C	leve
(3) LiPo	3.7V	5-10% / mês	não	500-1000	-40°C - 80°C	muito leve



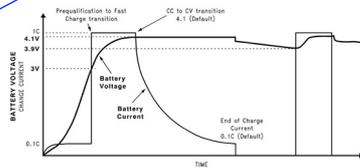
Baterias

- Densidade de energia



Baterias LiPo

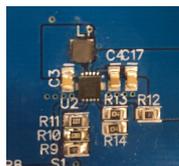
- Tensão de carga: < 4.2V
- Tensão de operação: > 3.0V
- Necessário proteção para:
 - sobre-carregamento (overcharge) (no carregador)
 - sobre-descarga (over-discharge) (na bateria)
 - sobre-temperatura (no carregador)
 - curto-circuito (na bateria)
- Carregador específico para LiPo e Li-ion



<https://www.digikey.com/en/articles/techzone/2016/sep/9-designer-guide-fast-lithium-ion-battery-charging>

Conversores DC-DC

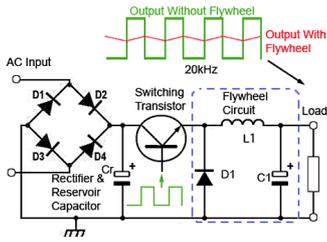
- Podem ser step-down (Buck), step-up (Boost), step-down/step-up (Buck/Boost)
- Alta eficiência (tip. > 80%)
- Ótima regulação de tensão e corrente
- Compacto (ICs dedicados com elemento de potência incluído, indutor externo)



Conversor DC-DC do Robô

Conversor Buck

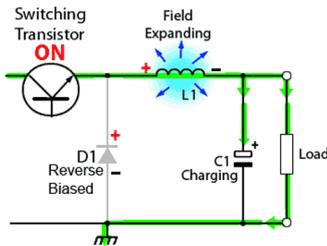
- Circuito básico



<http://www.learnabout-electronics.org/PSU/psu01.php>

Conversor Buck

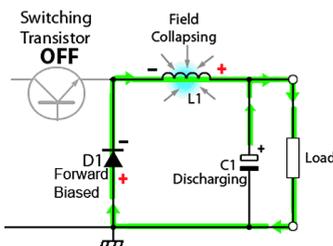
- Funcionamento



<http://www.learnabout-electronics.org/PSU/psu01.php>

Conversor Buck

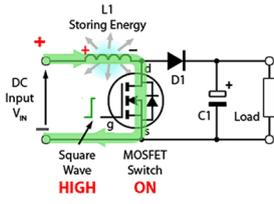
- Funcionamento



<http://www.learnabout-electronics.org/PSU/psu01.php>

Conversor Boost

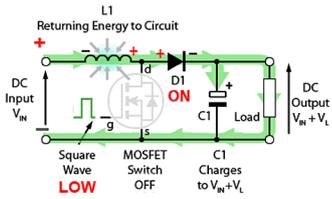
- Funcionamento



<http://www.learnabout-electronics.org/PSU/psu02.php>

Conversor Boost

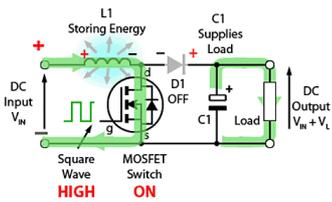
- Funcionamento



<http://www.learnabout-electronics.org/PSU/psu02.php>

Conversor Boost

- Funcionamento



<http://www.learnabout-electronics.org/PSU/psu02.php>

Projeto

- Seleção do valor indutor

$$L_1 = (V_{IN1} - V_{OUT}) \times 0.5 \times \frac{\mu S}{A}$$

- L_1 é o valor mínimo da indutância para modo Buck
 V_{IN1} é a tensão máxima da entrada

$$L_2 = V_{OUT} \times 0.5 \times \frac{\mu S}{A}$$

- L_2 é o valor mínimo da indutância para o modo Boost
- A indutância mínima é o maior entre L_1 e L_2

Projeto

- Determinação da corrente de pico em regime sobre o indutor

$$I_1 = \frac{I_{OUT}}{0.8} + \frac{V_{OUT}(V_{IN1} - V_{OUT})}{2 \times V_{IN1} \times f \times L}$$

$$I_2 = \frac{V_{OUT} \times I_{OUT}}{0.8 \times V_{IN2}} + \frac{V_{IN2} \times (V_{OUT} - V_{IN2})}{2 \times V_{OUT} \times f \times L}$$

- I_1 é a corrente em modo Buck
- I_2 é a corrente em modo Boost
- f é frequência de chaveamento
- V_{IN2} é a tensão mínima da entrada
- O valor da corrente crítica é o maior entre I_1 e I_2

Projeto

- O capacitor de entrada deve ser $\geq 4.7 \mu F$
 - Um capacitor cerâmico deve ser colocado o mais próximo possível dos pinos VIN e PGND
- Capacitor entre VINA e GND deve ser cerâmico de $0.1 \mu F$
 - O valor desse capacitor deve ser $\leq 0.22 \mu F$
- O valor mínimo do capacitor de saída pode ser calculado por:

$$C_{OUT} = 5 \times L \times \frac{\mu F}{\mu H}$$

- Esse capacitor deve ser colocado o mais próximo possível dos pino VOUT e PGND

