

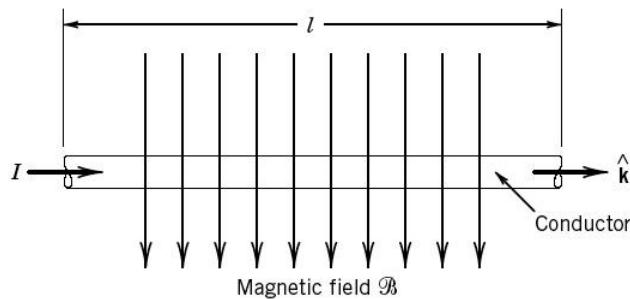
# Instrumentos de Medida

- Osciloscópio analógico
- Analizador de espectro
- Frequencímetro

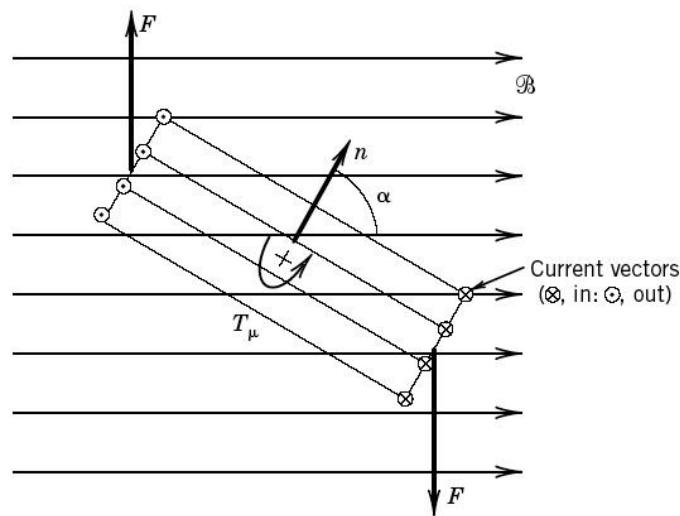
# Condicionamento de Sinais

- OPAMP
- Multiplexação analógica
- Demodulador Síncrono
- Filtros

Dispositivos analógicos de medição de corrente elétrica

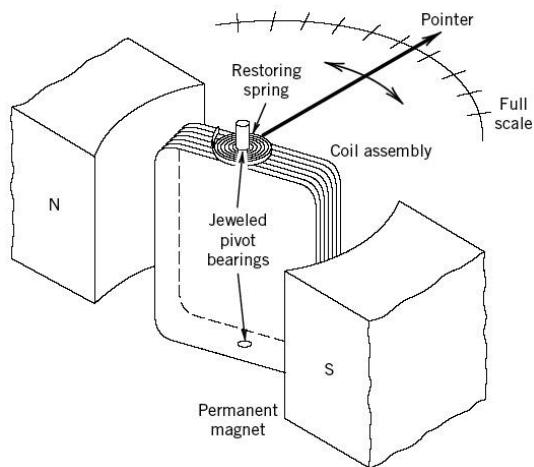


**Figure 6.1** Current-carrying conductor in a magnetic field.

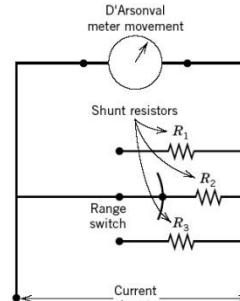


**Figure 6.2** Forces and resulting torque on a current loop in a magnetic field.

## Dispositivos analógicos de medição de corrente elétrica

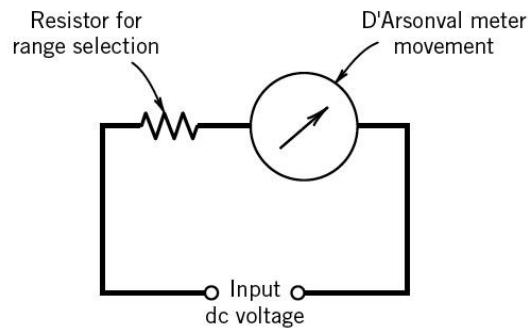


**Figure 6.3** Basic D'Arsonval meter movement.

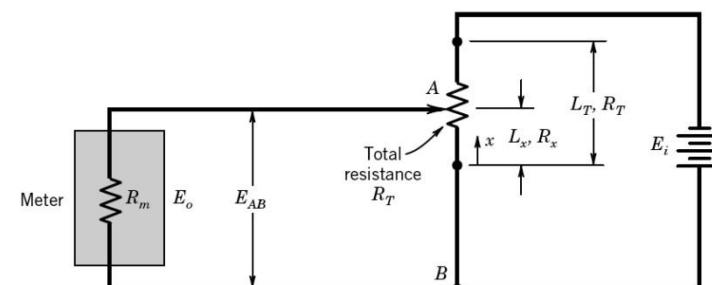


**Figure 6.4** Simple multirange ammeter (with make-before-break selector switch). Shunt resistors determine meter range.

## Dispositivos analógicos de medição de tensão elétrica



**Figure 6.6** A dc voltmeter circuit.



**Figure 6.9** Voltage divider circuit.

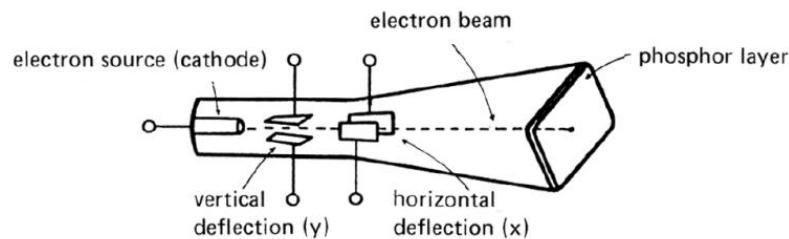
Exemplo de medida de um valor de resistência

Ligaçāo a jusante e a montante

Método de quatro fios

## Dispositivos analógicos de medição de tensão elétrica

### Osciloscópio



### Entrada de disparo - trigger

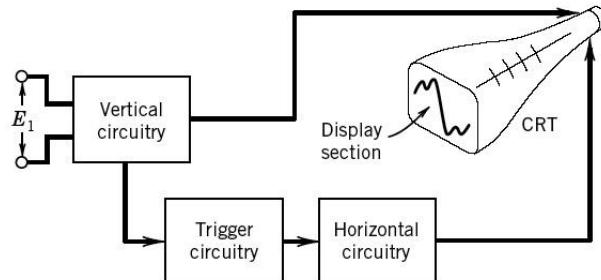


Figure 6.7 Schematic of basic cathod-ray tube oscilloscope.



(a)

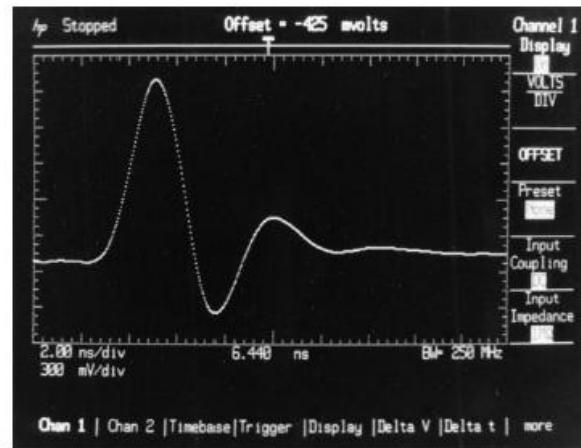


Figure 6.8 (a) Digital oscilloscope. (Photograph courtesy of Tektronix, Inc.) (b) Oscilloscope output. (Photograph courtesy of Hewlett-Packard Company.)

## Dispositivos analógicos de medição de resistência elétrica

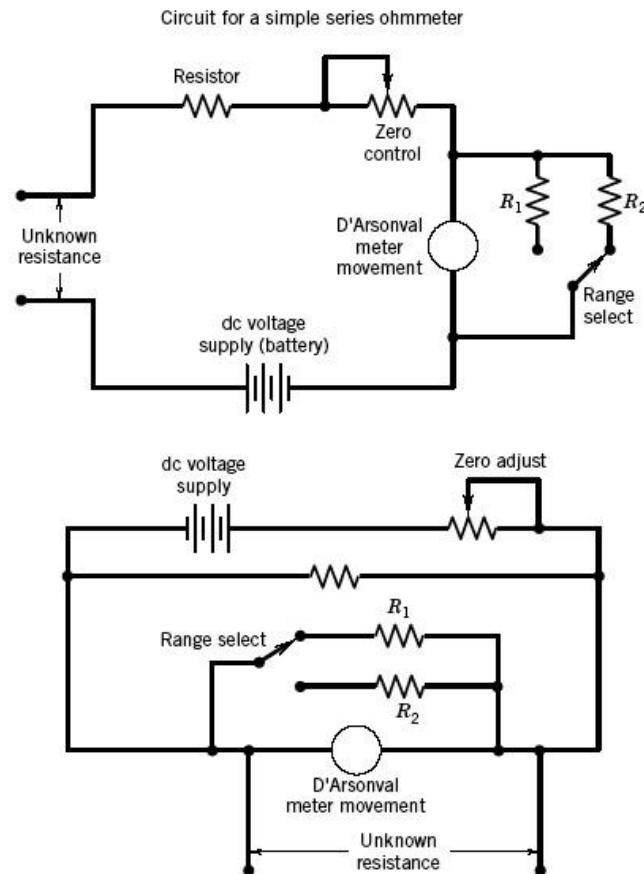


Figure 6.12 Multirange ohmmeter circuits.

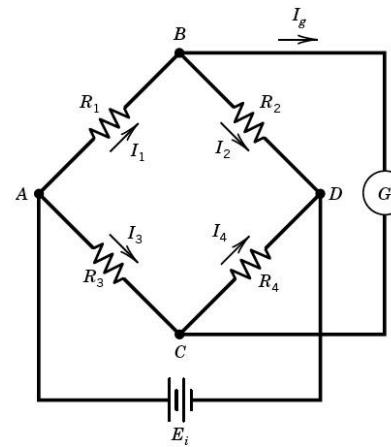


Figure 6.13 Basic Wheatstone bridge circuit ( $G$ , galvanometer).

Dispositivos analógicos de medição de tensão elétrica

## Osciloscópio

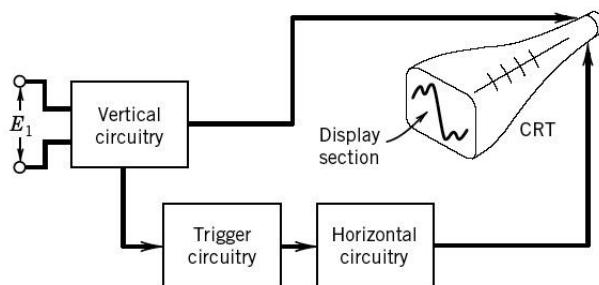
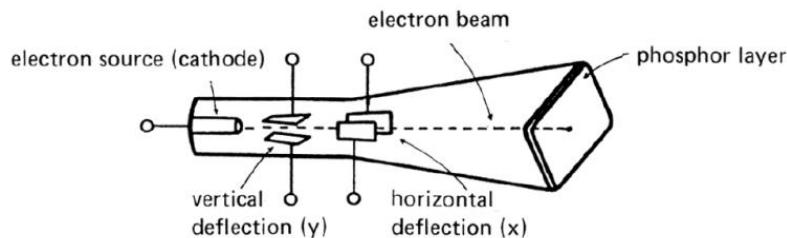


Figure 6.7 Schematic of basic cathod-ray tube oscilloscope.



(a)

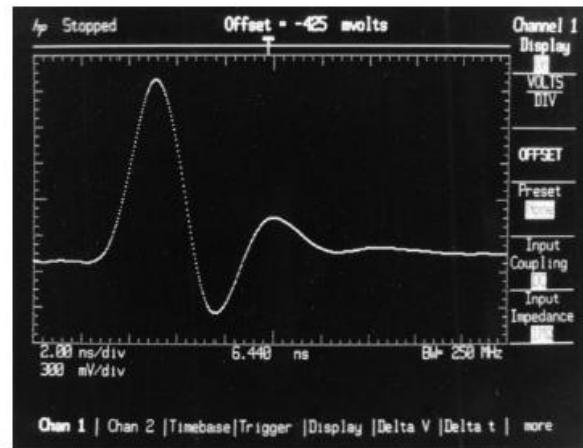
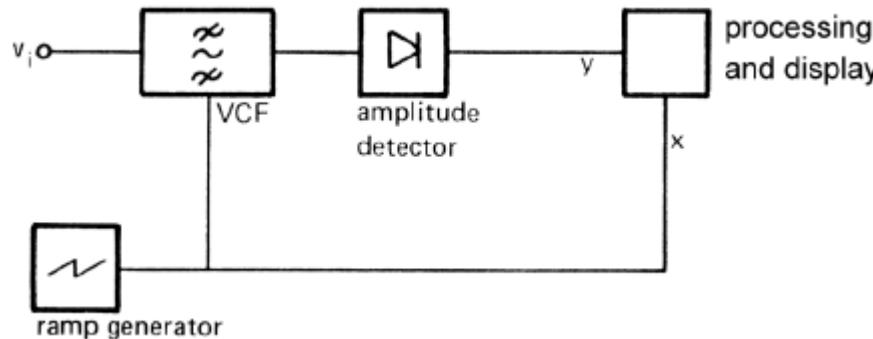


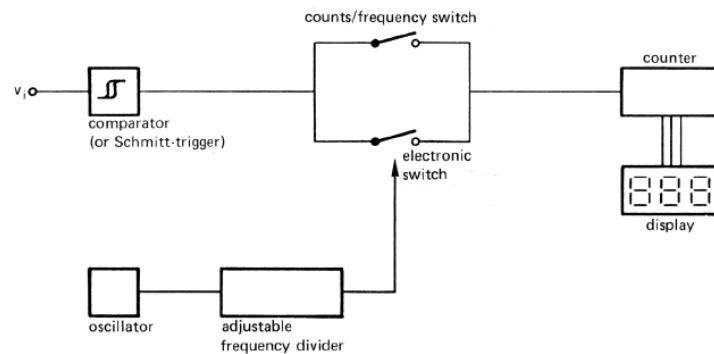
Figure 6.8 (a) Digital oscilloscope. (Photograph courtesy of Tektronix, Inc.) (b) Oscilloscope output. (Photograph courtesy of Hewlett-Packard Company.)

## Analisador de espectro



Medida de V, Vrms, P  
Casamento de impedância  
Linha de transmissão  
Espectro de potência  
Densidade espectral de potência

## Contador, frequencímetro, cronômetro



## Erros de carregamento

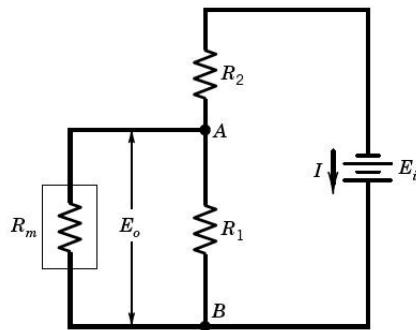


Figure 6.15 Instruments in parallel to signal path form an equivalent voltage dividing circuit.

Impedância de entrada de osciloscópios

Impedância de entrada em analisadores de espectro

Resistência interna de voltímetros

## Classificação de instrumentos de medição

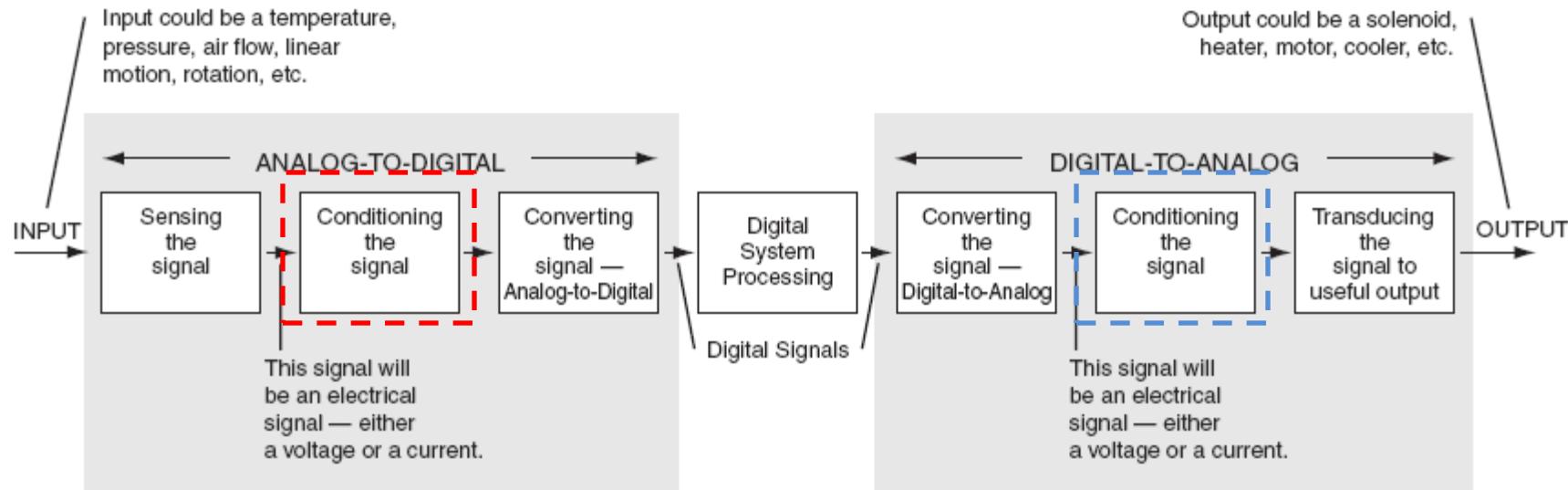
Ativo x Passivo

Anulação x Deflexão

Analógico x Digital

Indicadores x Sinal de saída

Autônomos x embarcados



- Adequação dos sinais de entrada a grandezas e faixas de valores de grandezas aceitáveis para a entrada do conversor AD
- Proporcionar amplificação do sinal de saída do conversor DA de modo a permitir o acionamento de dispositivos de atuação

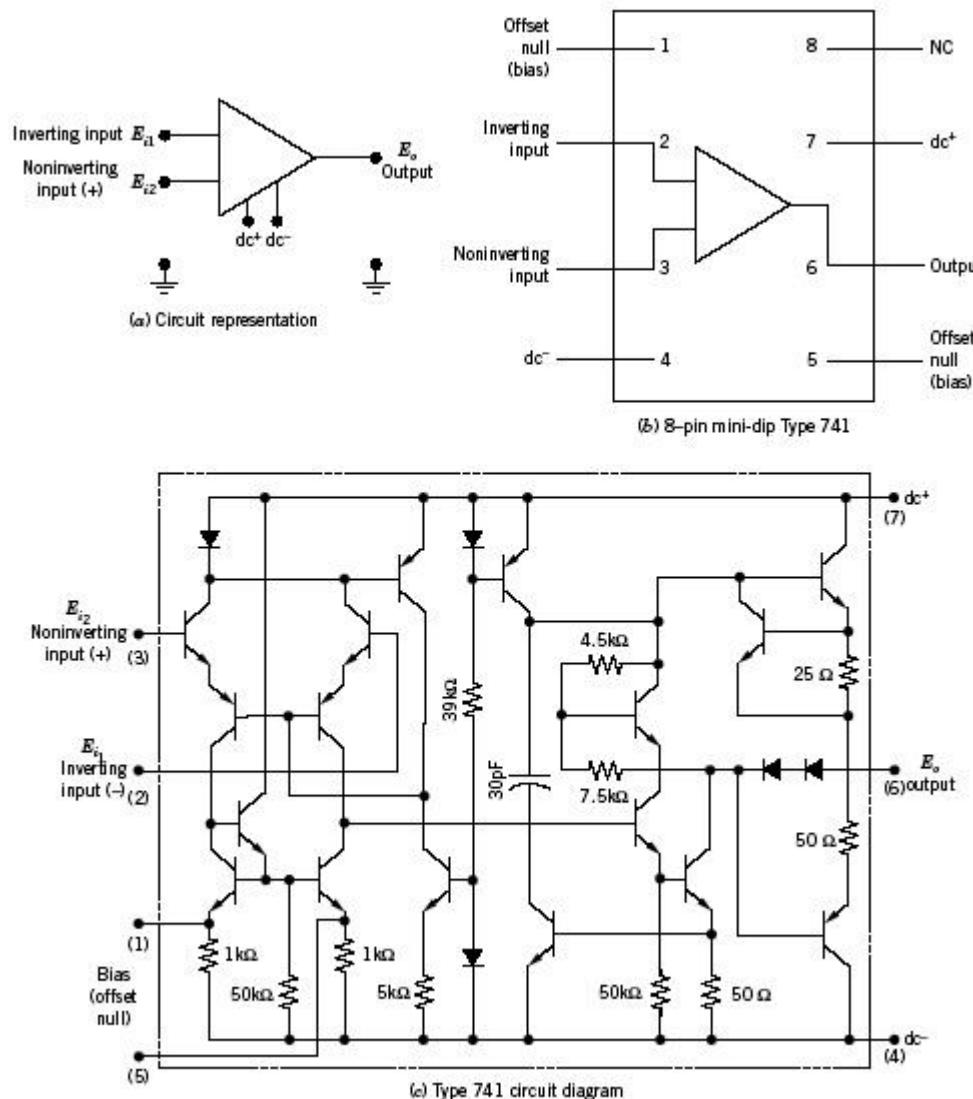
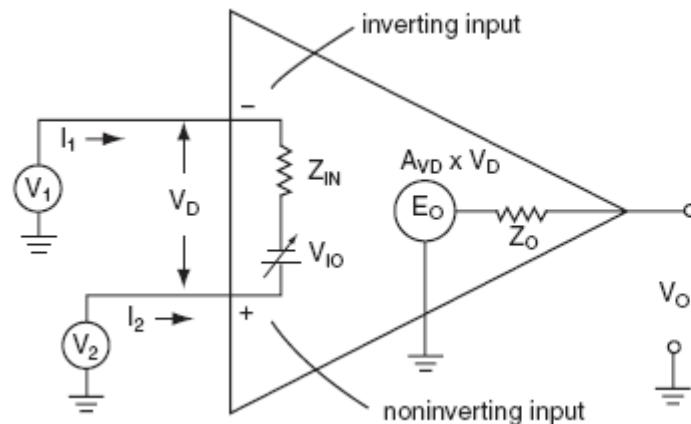


Figure 6.19 Operational amplifier.

# OPAMP



$I_1, I_2$  = Input currents

$V_D$  = Differential input voltage

$Z_{IN}$  = Input impedance

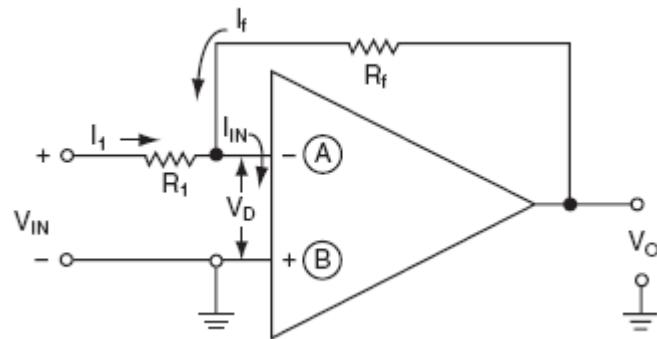
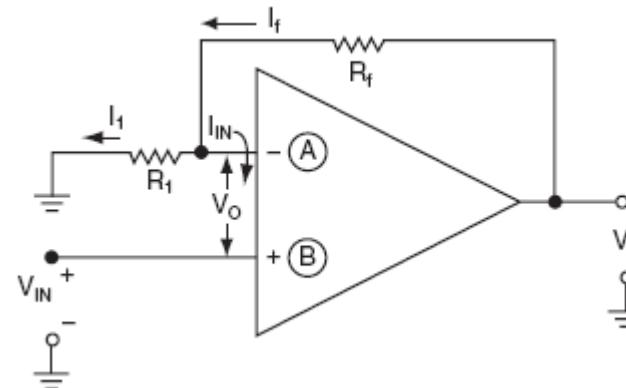
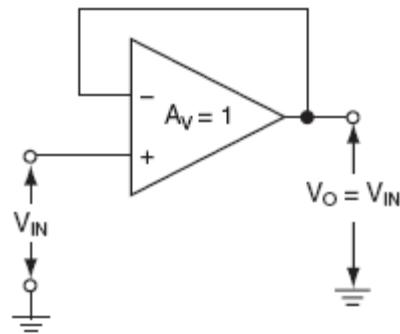
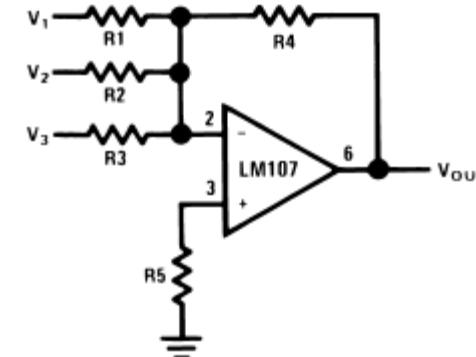
$V_{IO}$  = Input offset voltage

$A_{VD}$  = Open-loop differential voltage gain

$Z_O$  = Output impedance

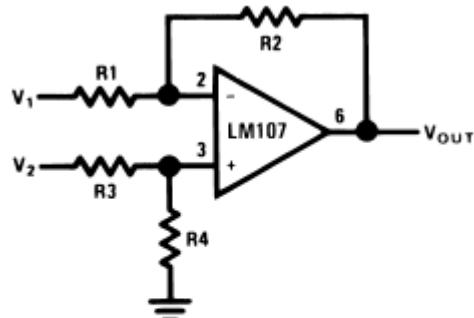
$V_O$  = Output voltage

- Ganho de malha aberta muito alto - A*
- Alta razão de rejeição de modo comum – CMRR*
- Alta impedância de entrada*
- Baixa impedância de saída*

*Configurações simples**Amplif. Inversor**Amplif. Não-Inversor**Seguidor de tensão**Somador*

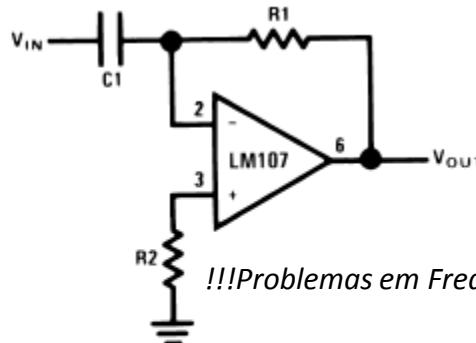
00682204

$$V_{OUT} = -R_4 \left( \frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} \right)$$

**OPAMP***Amplif. de Diferença*

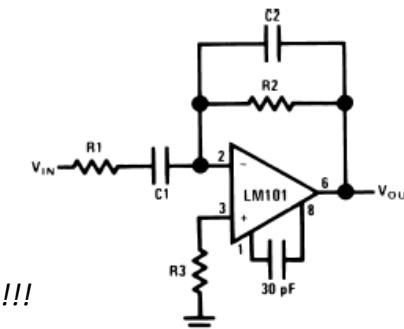
00682205

$$V_{\text{OUT}} = \left( \frac{R_1 + R_2}{R_3 + R_4} \right) \frac{R_4}{R_1} V_2 - \frac{R_2}{R_1} V_1$$

*Diferenciador*

00682206

$$V_{\text{OUT}} = -R_1 C_1 \frac{d}{dt} (V_{\text{IN}})$$

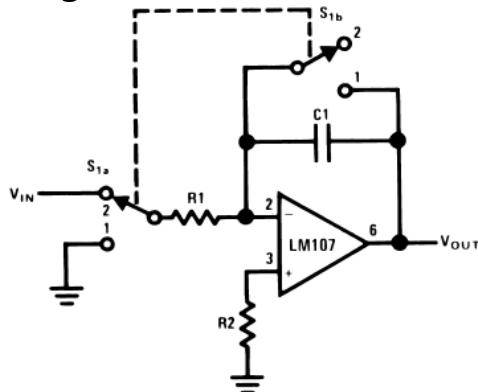


00682207

$$f_c = \frac{1}{2\pi R_2 C_1}$$

$$f_h = \frac{1}{2\pi R_1 C_1} = \frac{1}{2\pi R_2 C_2}$$

$$f_c \ll f_h \ll f_{\text{unity gain}}$$

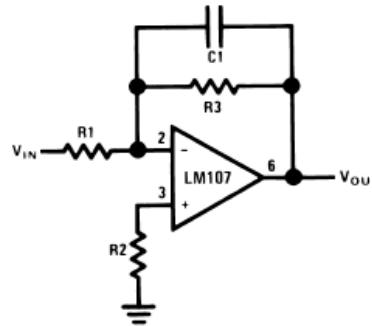
*Integrador*

00682209

$$V_{\text{OUT}} = \frac{1}{R_1 C_1} \int_{t_1}^{t_2} V_{\text{IN}} dt$$

$$f_c = \frac{1}{2\pi R_1 C_1}$$

$$R_1 = R_2$$

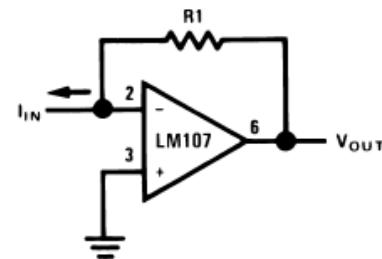
*Filtro PB*

00682211

$$f_L = \frac{1}{2\pi R_1 C_1}$$

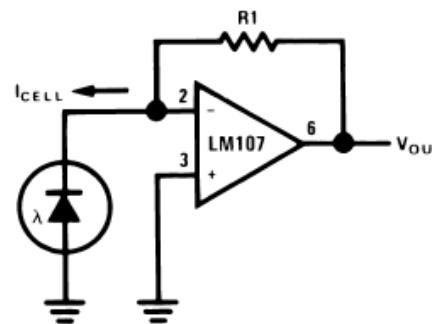
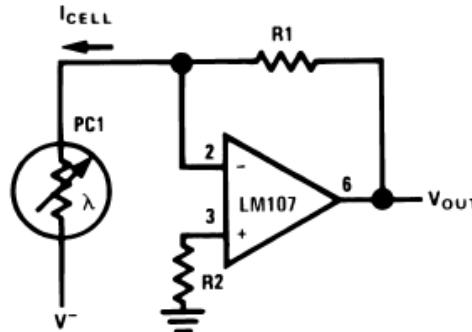
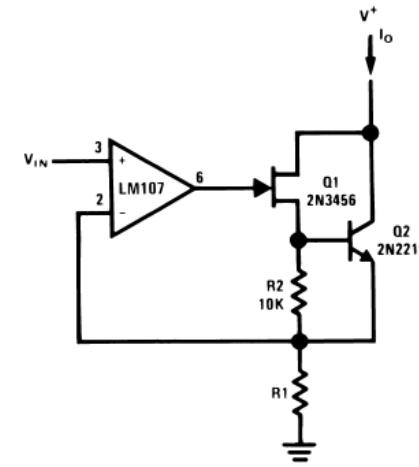
$$f_c = \frac{1}{2\pi R_3 C_1}$$

$$A_L = \frac{R_3}{R_1}$$

*Conversor I-V*

00682213

$$V_{\text{OUT}} = I_{\text{IN}} R_1$$

**OPAMP***Amplif. p/ Célula Fotocondutora e fotovoltaica**Fonte de Corrente*

00682217

$$I_O = \frac{V_{IN}}{R_1}$$

**An Applications Guide for  
Op Amps**

National Semiconductor  
Application Note 20  
February 1969

## Características Importantes (Exemplo de caso)

### ABSOLUTE MAXIMUM RATINGS (Note 4)

Supply Voltage	.....	$\pm 22V$
Input Voltage (Note 1)	.....	$\pm 22V$
Output Short-Circuit Duration	.....	Indefinite
Differential Input Voltage (Note 2)	.....	$\pm 0.7V$
Differential Input Current (Note 2)	.....	$\pm 25mA$
Storage Temperature Range	.....	$-65^{\circ}C$ to $+150^{\circ}C$

### Operating Temperature Range

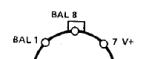
OP-27A, OP-27B, OP-27C (J, Z, RC).....	$-55^{\circ}C$ to $+125^{\circ}C$
OP-27E, OP-27F (J, Z).....	$-25^{\circ}C$ to $+85^{\circ}C$
OP-27E, OP-27F (P).....	$0^{\circ}C$ to $+70^{\circ}C$
OP-27G (P, S, J, Z).....	$-40^{\circ}C$ to $+85^{\circ}C$

Lead Temperature Range (Soldering, 60 sec)	.....	$300^{\circ}C$
Junction Temperature	.....	$-65^{\circ}C$ to $+150^{\circ}C$

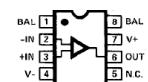
PACKAGE TYPE	$\theta_{JA}$ (Note 3)	$\theta_{JC}$	UNITS
TO-99 (J)	150	18	$^{\circ}C/W$
8-Pin Hermetic DIP (Z)	148	16	$^{\circ}C/W$
8-Pin Plastic DIP (P)	103	43	$^{\circ}C/W$
20-Contact LCC (RC)	98	38	$^{\circ}C/W$
8-Pin SO (S)	158	43	$^{\circ}C/W$

### NOTES:

- For supply voltages less than  $\pm 22V$ , the absolute maximum input voltage is equal to the supply voltage.
- The OP-27's inputs are protected by back-to-back diodes. Current limiting resistors are not used in order to achieve low noise. If differential input voltage exceeds  $\pm 0.7V$ , the input current should be limited to  $25mA$ .
- $\theta_{JA}$  is specified for worst case mounting conditions, i.e.,  $\theta_{JA}$  is specified for device in socket for TO, CerDIP, P-DIP, and LCC packages;  $\theta_{JA}$  is specified for device soldered to printed circuit board for SO package.
- Absolute maximum ratings apply to both DICE and packaged parts, unless otherwise noted.

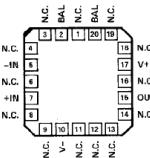


TO-99  
(J-Suffix)



8-PIN HERMETIC DIP  
(Z-Suffix)

EPOXY MINI-DIP  
(P-Suffix)



8-PIN SO  
(S-Suffix)

OP-27BRC/883  
LCC PACKAGE  
(RC-Suffix)



Low Noise, Precision  
Operational Amplifier

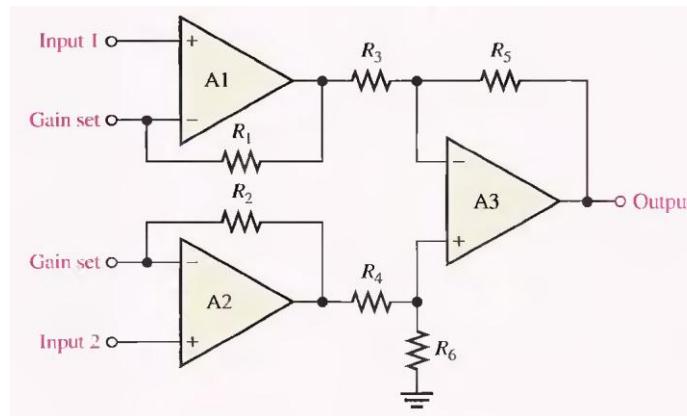
OP-27

ELECTRICAL CHARACTERISTICS at  $V_S = \pm 15V$ ,  $T_A = 25^{\circ}C$ , unless otherwise noted.

PARAMETER	SYMBOL	CONDITIONS	OP-27A/E			OP-27B/F			OP-27C/G			
			MIN	Typ	MAX	MIN	Typ	MAX	MIN	Typ	MAX	
Input Offset Voltage	$V_{OS}$	(Note 1)	—	10	25	—	20	60	—	30	100	
Long-Term $V_{OS}$ Stability	$V_{OS}/\text{Time}$	(Notes 2, 3)	—	0.2	1.0	—	0.3	1.5	—	0.4	2.0	
Input Offset Current	$I_{OS}$	—	—	7	35	—	9	50	—	12	75	
Input Bias Current	$I_B$	—	$\pm 10$	$\pm 40$	—	$\pm 12$	$\pm 55$	—	$\pm 15$	$\pm 80$	nA	
Input Noise Voltage	$e_{NP-P}$	0.1Hz to 10Hz (Notes 3, 5)	—	0.08	0.18	—	0.08	0.18	—	0.09	0.25	$\mu V_{p-p}$
Input Noise Voltage Density	$e_n$	$f_0 = 10Hz$ (Note 3) $f_0 = 30Hz$ (Note 3) $f_0 = 1000Hz$ (Note 3)	—	3.5	5.5	—	3.5	5.5	—	3.8	8.0	
Input Noise Current Density	$i_n$	$f_0 = 10Hz$ (Notes 3, 6) $f_0 = 30Hz$ (Notes 3, 6) $f_0 = 1000Hz$ (Notes 3, 6)	—	1.7	4.0	—	1.7	4.0	—	1.7	—	
Input Resistance — Differential-Mode	$R_{IN}$	(Note 7)	1.3	6	—	0.94	5	—	0.7	4	—	
Input Resistance — Common-Mode	$R_{INCM}$	—	3	—	—	2.5	—	—	2	—	GΩ	
Input Voltage Range	IVR	—	$\pm 11.0$	$\pm 12.3$	—	$\pm 11.0$	$\pm 12.3$	—	$\pm 11.0$	$\pm 12.3$	—	
Common-Mode Rejection Ratio	CMRR	$V_{CM} = \pm 1V$	114	126	—	106	123	—	100	120	—	
Power Supply Rejection Ratio	PSRR	$V_S = \pm 4V$ to $\pm 18V$	—	1	10	—	1	10	—	2	20	$\mu V/V$
Large-Signal Voltage Gain	$A_{VO}$	$R_L \geq 2k\Omega$ , $V_O = \pm 10V$ $R_L \geq 600\Omega$ , $V_O = \pm 10V$	1000	1800	—	1000	1800	—	700	1500	—	
Output Voltage Swing	$V_O$	$R_L \geq 2k\Omega$ $R_L \geq 600\Omega$	$\pm 12.0$	$\pm 13.8$	—	$\pm 12.0$	$\pm 13.8$	—	$\pm 11.5$	$\pm 13.5$	—	
Slew Rate	SR	$R_L \geq 2k\Omega$ (Note 4)	1.7	2.8	—	1.7	2.8	—	1.7	2.8	—	
											$V/\mu s$	

ELECTRICAL CHARACTERISTICS at  $V_S = \pm 15V$ ,  $T_A = 25^{\circ}C$ , unless otherwise noted. (Continued)

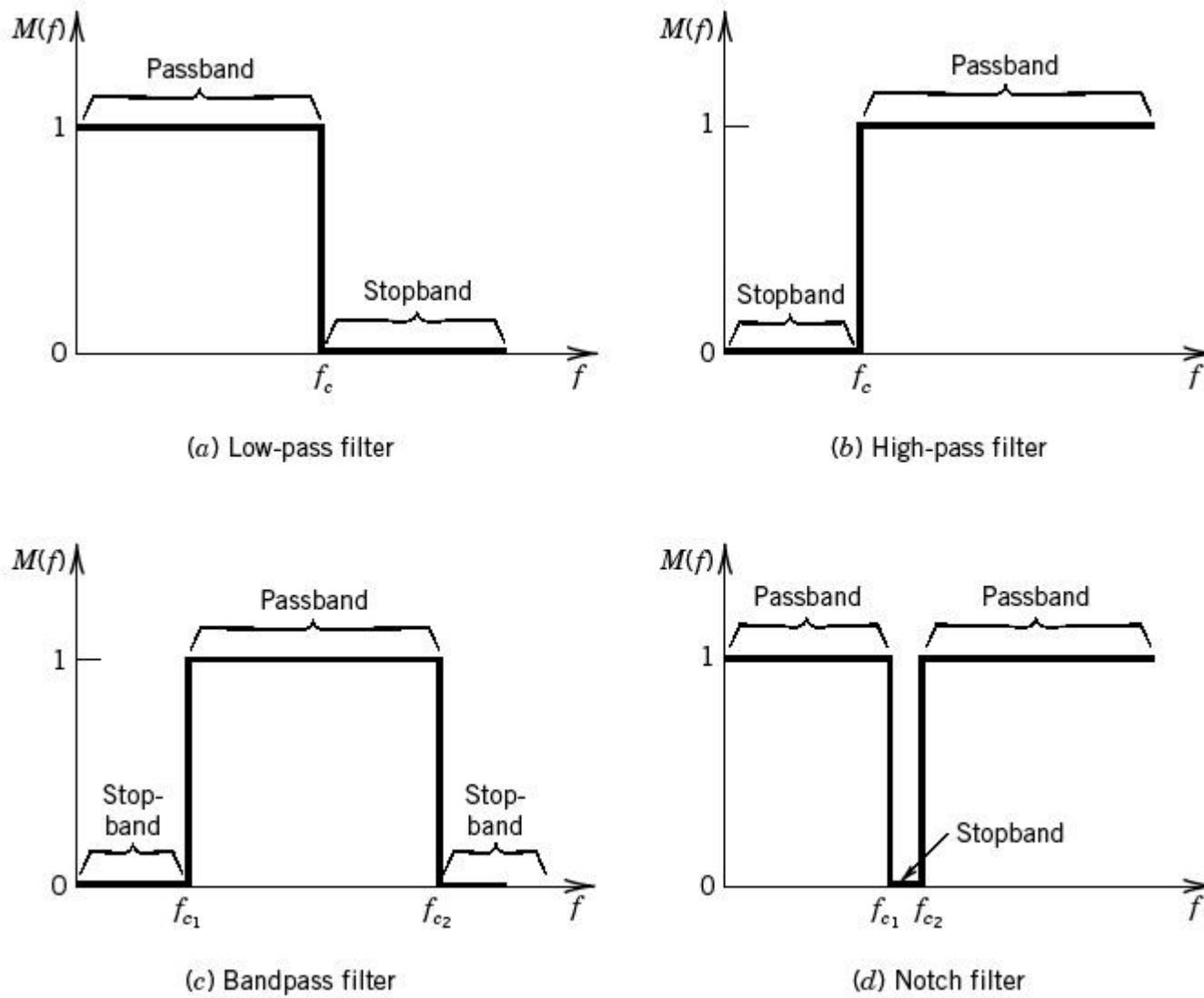
PARAMETER	SYMBOL	CONDITIONS	OP-27A/E			OP-27B/F			OP-27C/G		
			MIN	Typ	MAX	MIN	Typ	MAX	MIN	Typ	MAX
Gain Bandwidth Prod. GBW	(Note 4)	—	5.0	8.0	—	5.0	8.0	—	5.0	8.0	—
Open-Loop Output Resistance	$R_O$	$V_O = 0$ , $I_O = 0$	—	70	—	—	70	—	—	70	—
Power Consumption	$P_d$	$V_O$	—	90	140	—	90	140	—	100	170
Offset Adjustment Range	$R_P$	$= 10k\Omega$	—	$\pm 4.0$	—	—	$\pm 4.0$	—	—	$\pm 4.0$	—
											mV

*Amplificador de Instrumentação*

$$A_{cl} = 1 + \frac{2R}{R_G}$$

Alta impedância de entrada  
Rejeição de modo comum

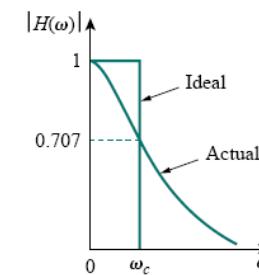
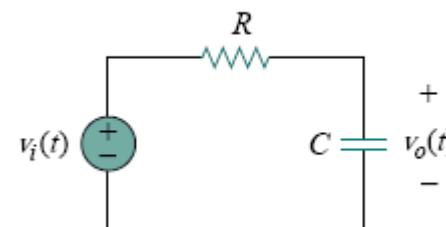
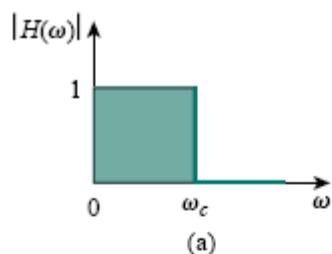
# Filtros



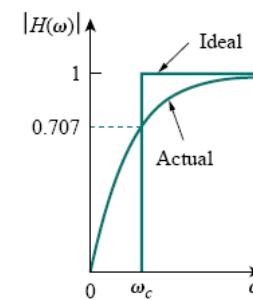
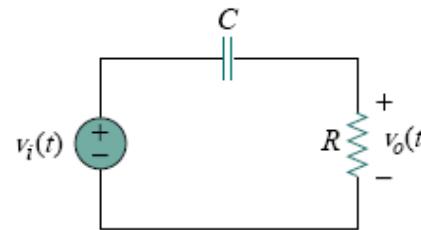
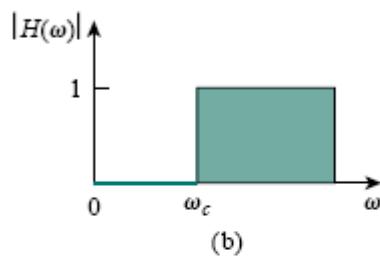
**Figure 6.27** Ideal filter characteristics.

# Filtros

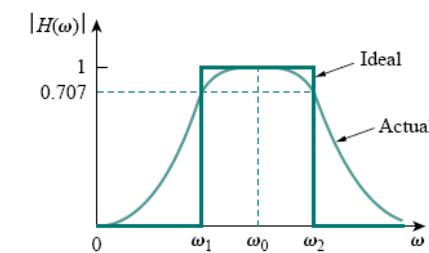
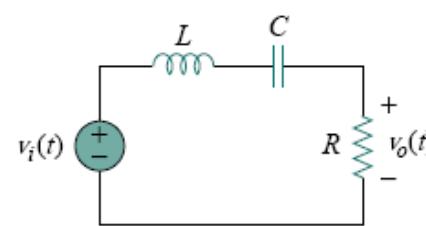
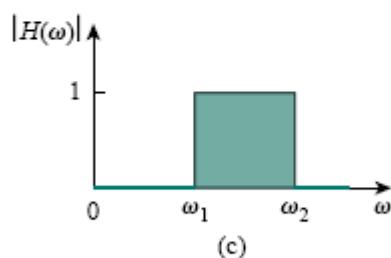
## Passa-baixa



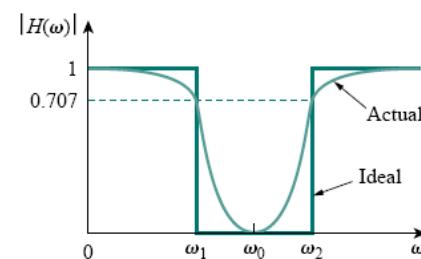
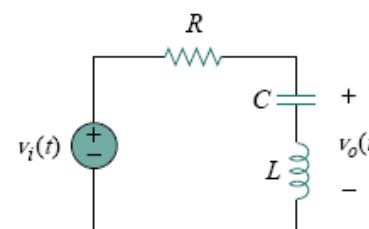
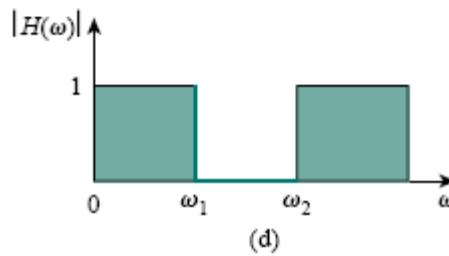
## Passa-alta



## Passa-banda



## Rejeita-banda



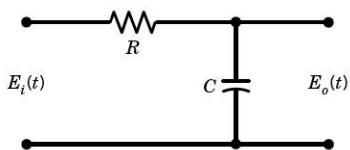


Figure 6.29 Low-pass RC Butterworth filter circuit.

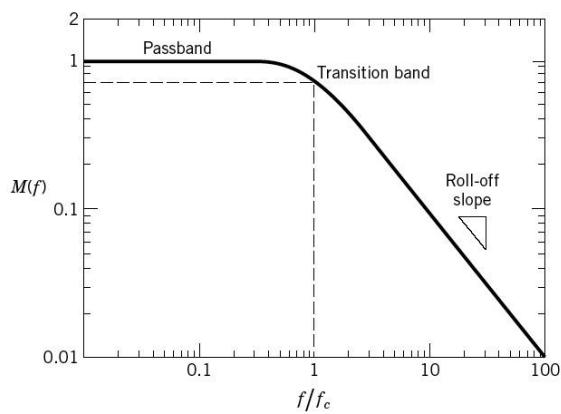


Figure 6.28 Magnitude ratio for a low-pass Butterworth filter.

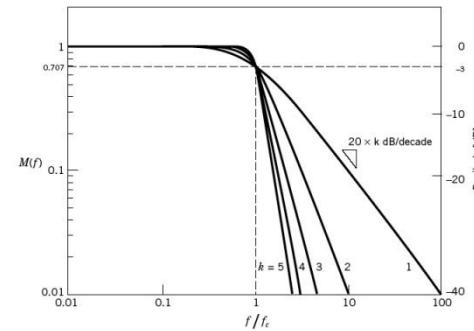
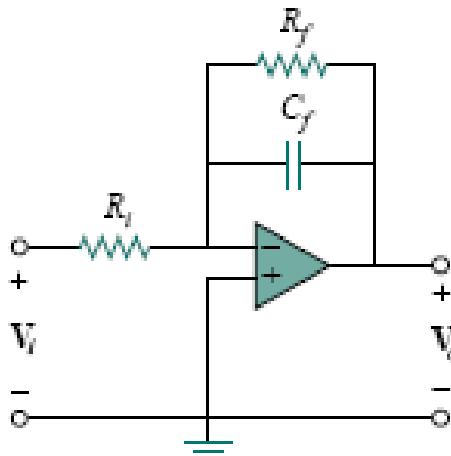
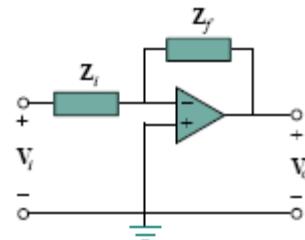
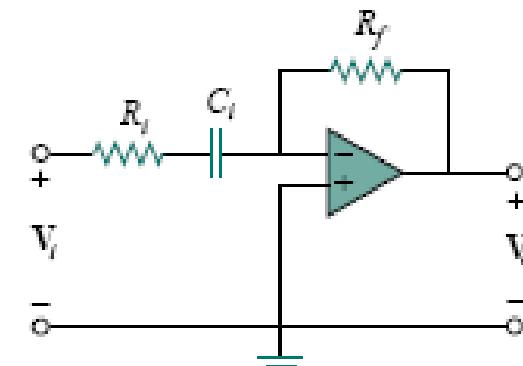


Figure 6.31 Magnitude characteristics for Butterworth low-pass filters of various stages.

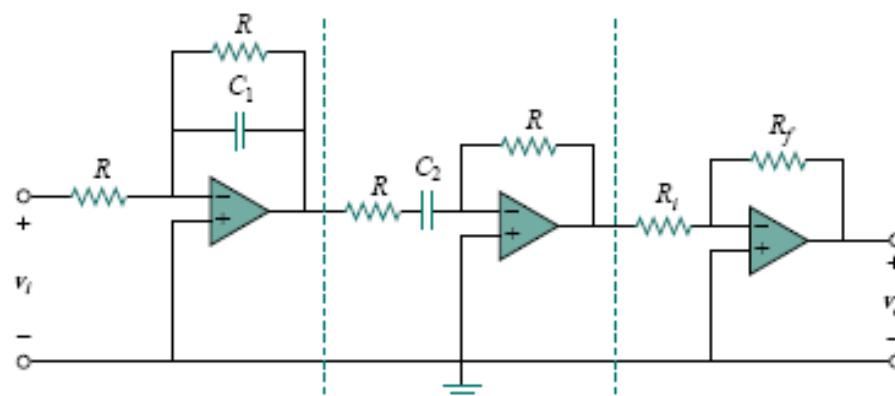
# Filtros



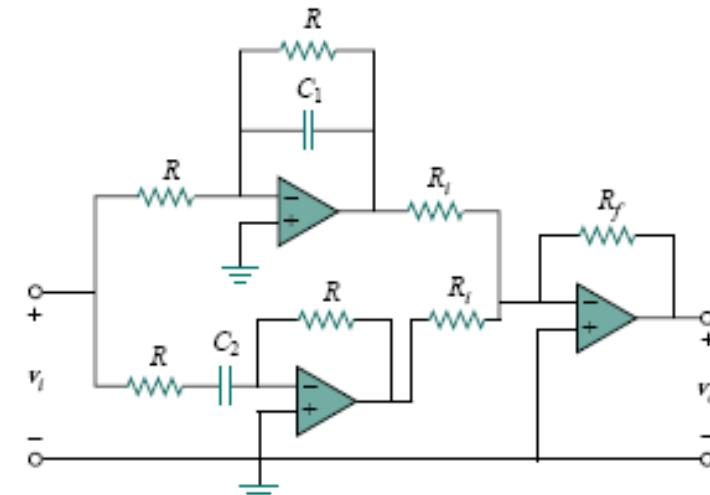
Passa-baixa



Passa-alta



Passa-banda

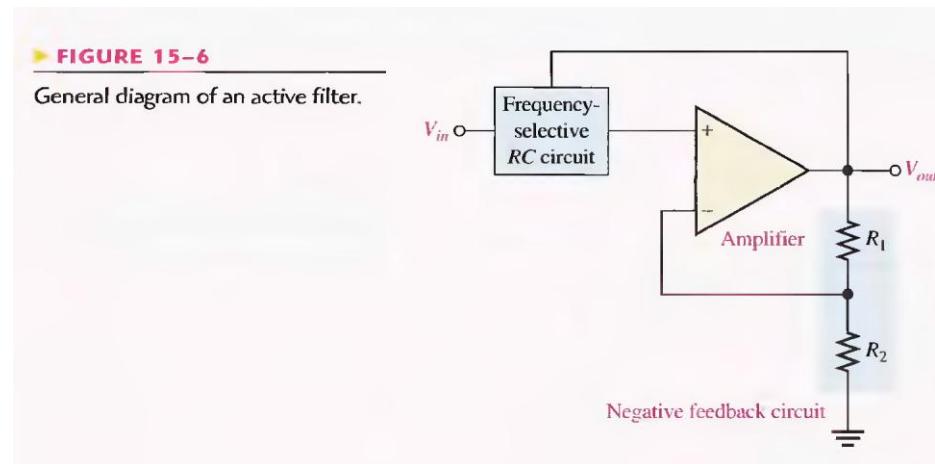
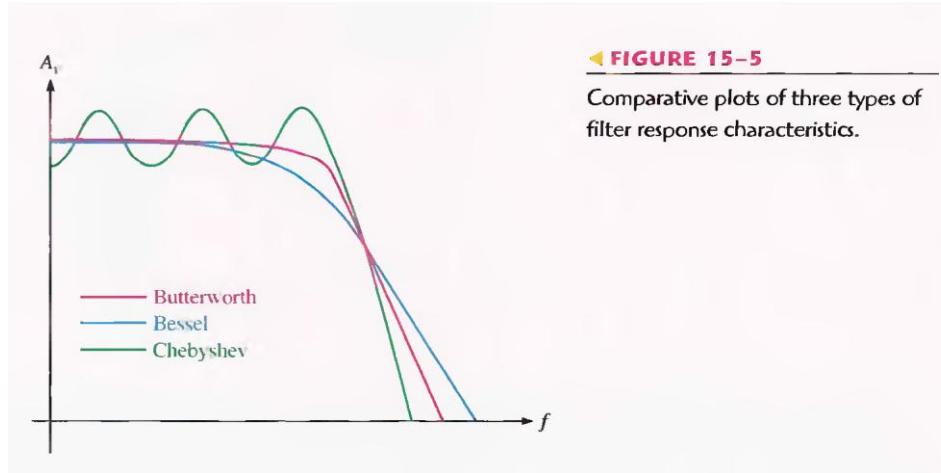


Rejeita-banda

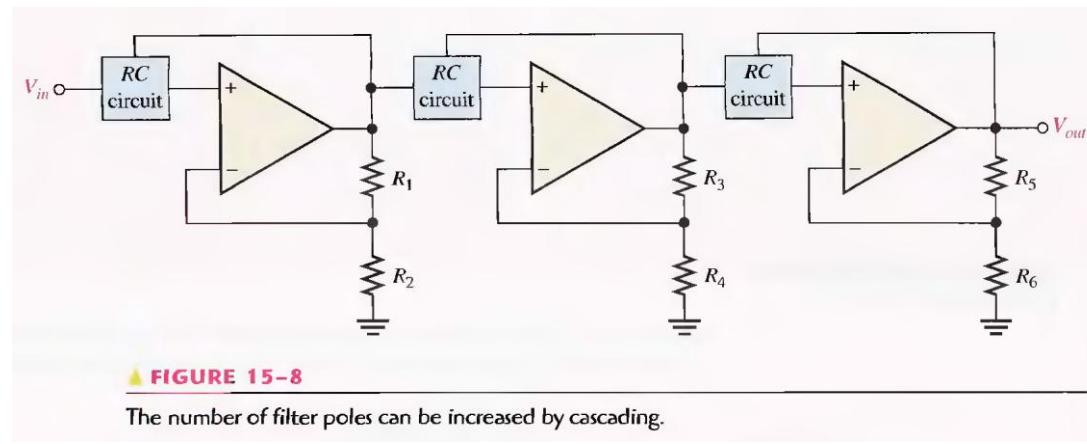
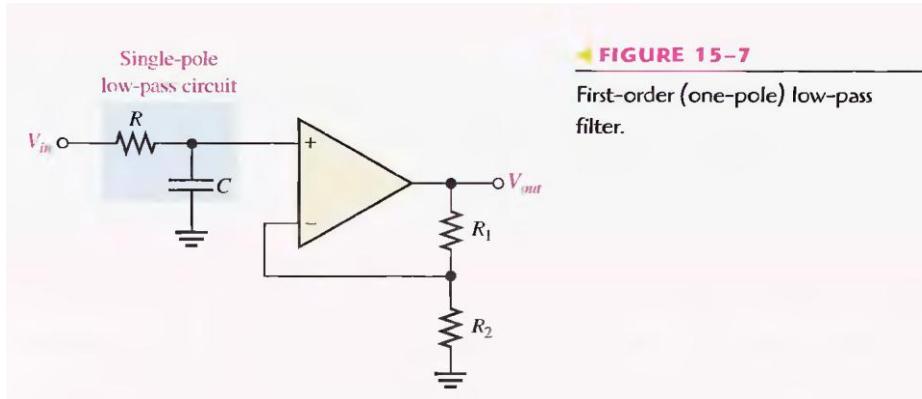
**Respostas em frequência afetam amplitude e fase**

# Filtros

## Butterworth, Bessel, Chebyshev



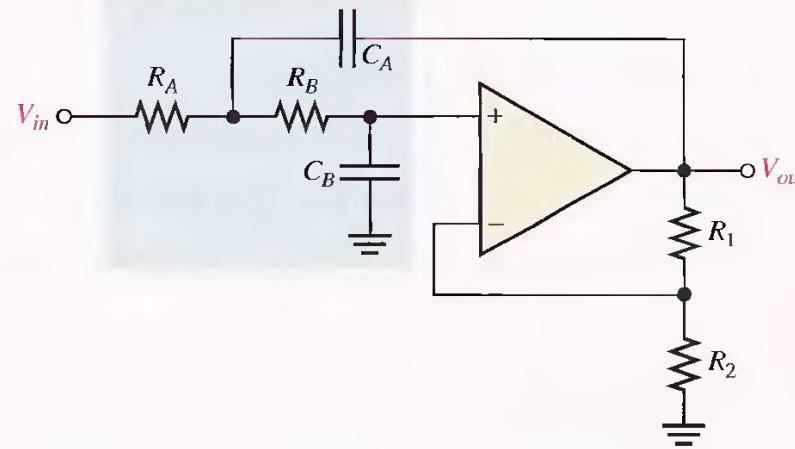
# Filtros



## Filtro PB Sallen-Key

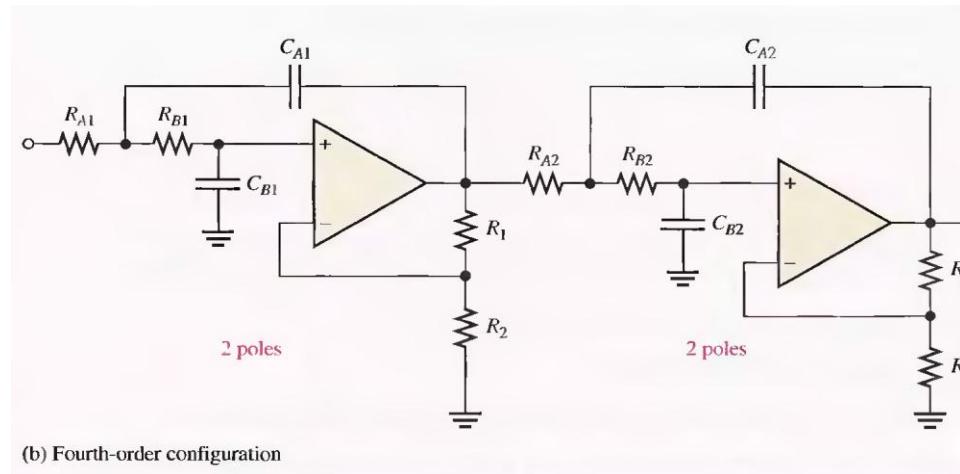
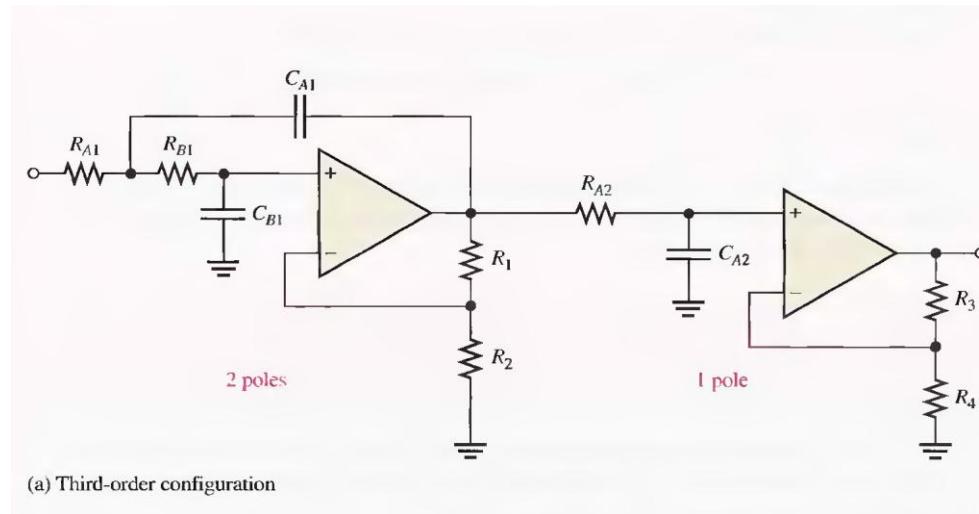
$$f_c = \frac{1}{2\pi\sqrt{R_A R_B C_A C_B}}$$

Two-pole low-pass circuit



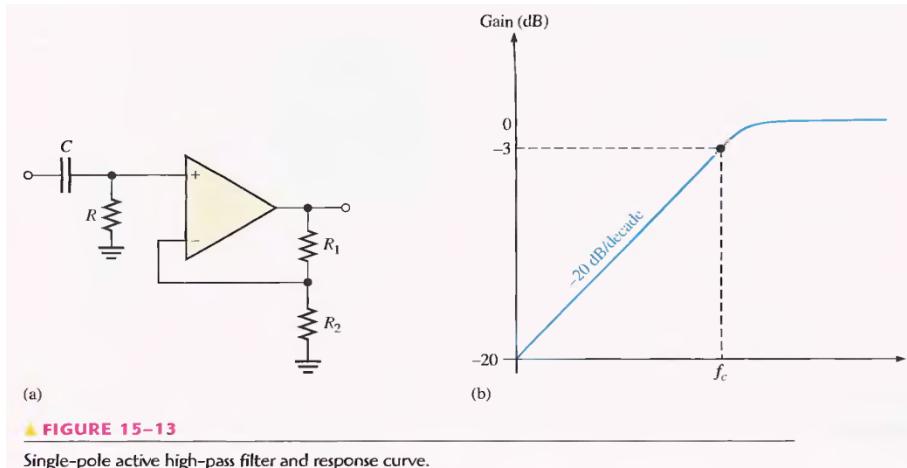
# Filtros

## Filtros em cascata



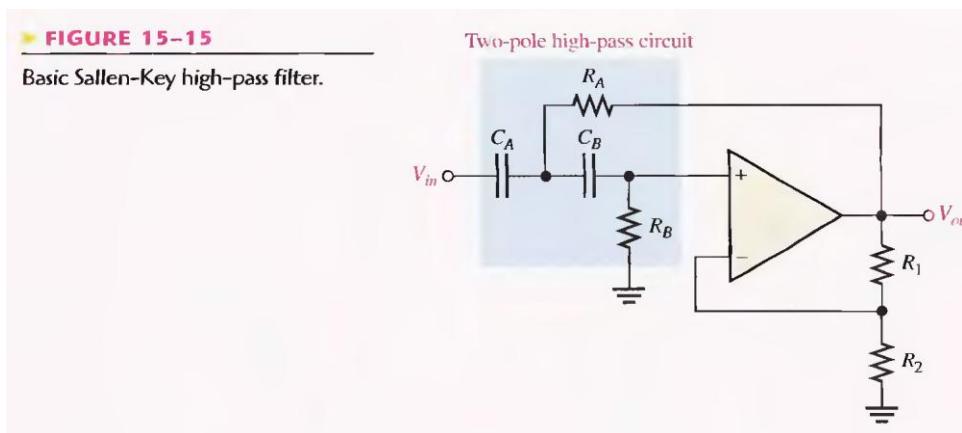
# Filtros

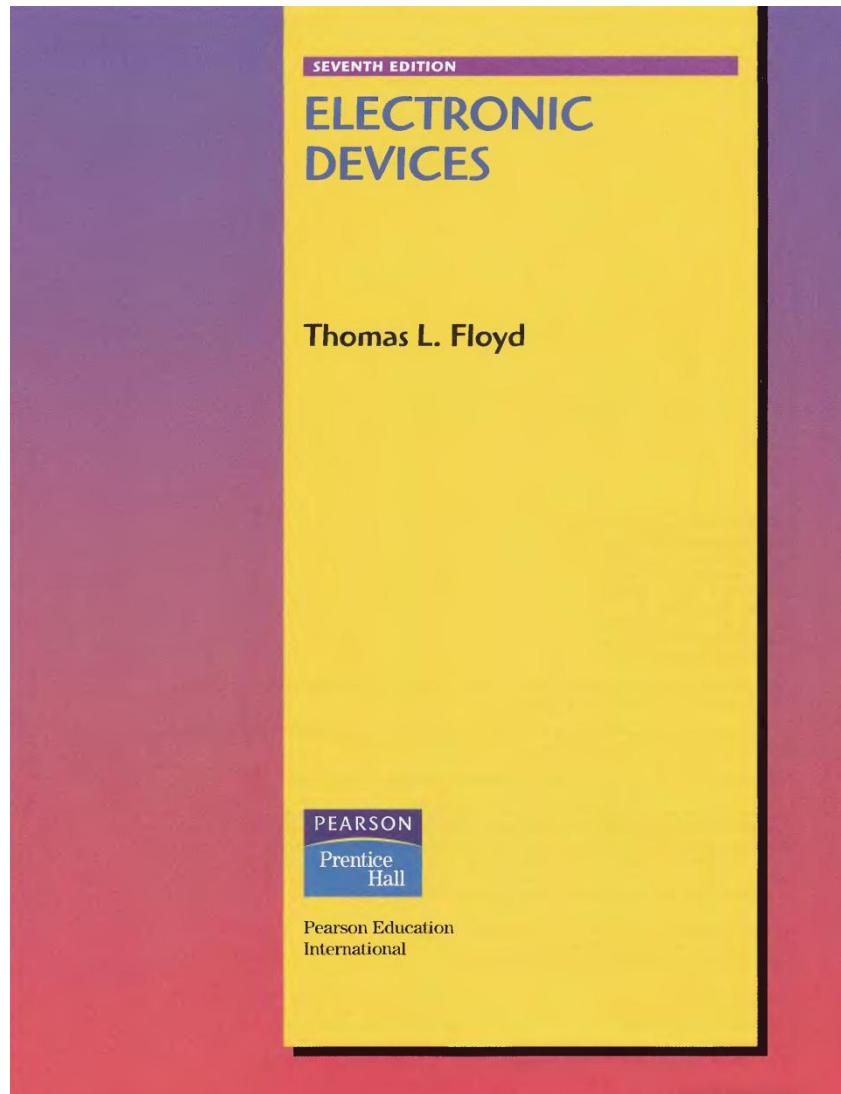
## Filtros PA



▲ FIGURE 15-13

Single-pole active high-pass filter and response curve.

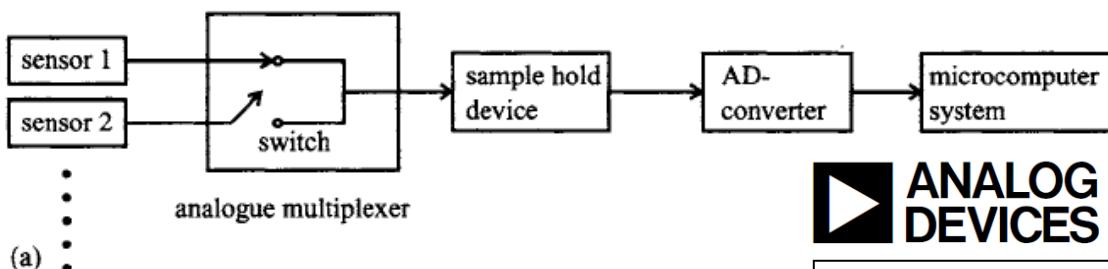
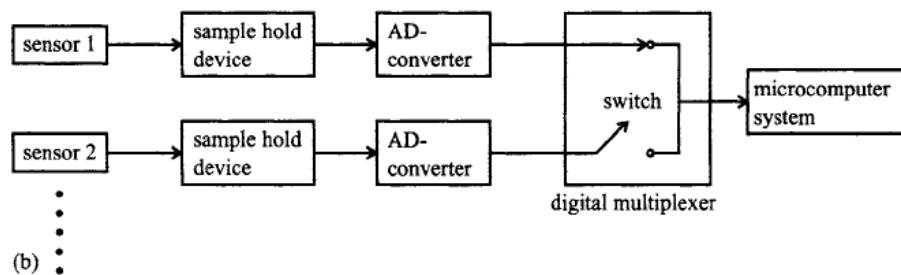




# Multiplexação Analógica

SEM0539 - Instrumentação e Sistemas de Medidas

## Aquisição de Sinais de Vários Sensores



ANALOG  
DEVICES

LC<sup>2</sup>MOS Precision  
Mini-DIP Analog Switch  
ADG419

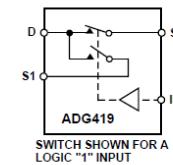
### FEATURES

- 44 V Supply Maximum Ratings
- V<sub>SS</sub> to V<sub>DD</sub> Analog Signal Range
- Low On Resistance (< 35 Ω)
- Ultralow Power Dissipation (< 35 μW)
- Fast Transition Time (160 ns max)
- Break-Before-Make Switching Action
- Plug-In Replacement for DG419

### APPLICATIONS

- Precision Test Equipment
- Precision Instrumentation
- Battery Powered Systems
- Sample Hold Systems

### FUNCTIONAL BLOCK DIAGRAM



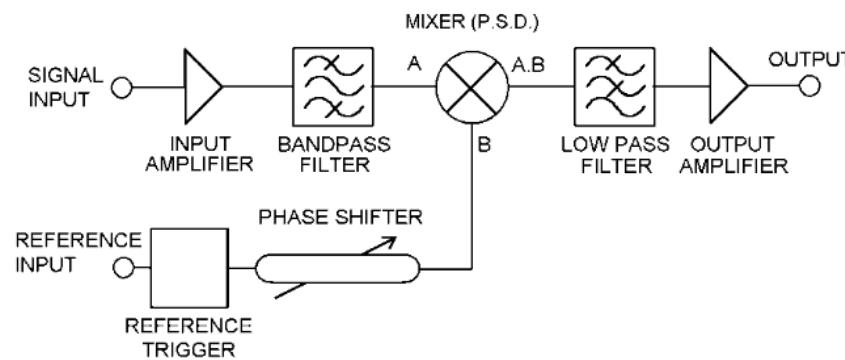
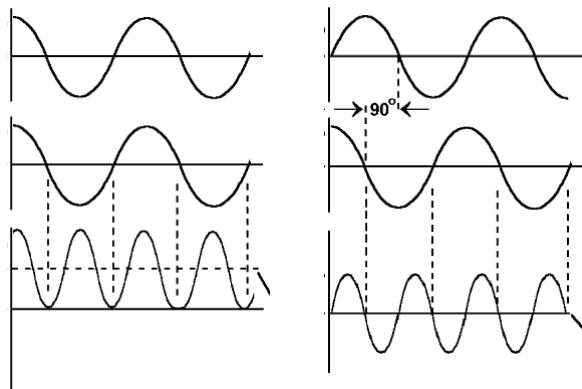
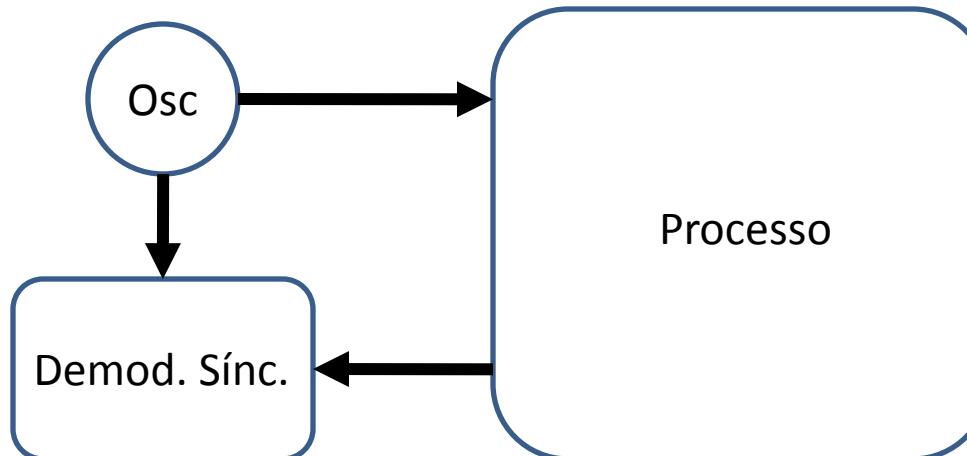
SWITCH SHOWN FOR A  
LOGIC "1" INPUT

# Demodulação Síncrona

Sinais muito pequenos

Modulação injetada no sistema

Demodulação sensível à fase do sinal medido



# Demodulação Síncrona

